

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: Unassigned
Examiner: Unassigned

In Re PATENT APPLICATION Of:


Applicant(s)	:	KUMAMOTO et al.)	
)	
Serial No.	:	Divisional of Appl. Serial No. 09/504,874)	
)	
Filed	:	January 30, 2004)	INFORMATION
)	<u>DISCLOSURE</u>
For	:	SEMICONDUCTOR CHIP AND)	
		PRODUCTION PROCESS THEREFOR)	
)	
Attorney Ref.	:	AI 318 D1)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, attached are copies of the Forms PTO-892 from the parent application, serial number, **09/504,874**, which the Examiner may wish to consult during examination of this Divisional Application thereof.

Respectfully submitted,



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January 30, 2004
Date

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Notice of References Cited	Applicant/Patent Kumamoto et al	Application/Control No. 09/504,874	
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U.S. PATENT DOCUMENTS

	Document Number <small>Country Code-Number-Kind Code</small>	Date <small>MM-YYYY¹</small>	Name	Classification ²	
X	A 5,821,625	10/1998	Yoshida et al	257	777
X	B 5,072,331	12/1991	Thiele et al	361	380
X	C 5,360,988	11/1994	Uda et al	257	529
X	D 6,130,483	10/2000	Shizuki et al	257	778
X	E 6,025,647	2/2000	Shenoy et al	257	775
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¹ Dates in MM-YYYY format are publication dates.

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✓	C	6,064,114	05/2000	Higgins III	257	698
✓	D	6,107,685	08/2000	Nishiyama	257	737
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